

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(@ad<"20030626" @rlad<"20030626") and PCB and (microchip chip ASIC FPGA) with incompatibility with disable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/12 14:22
L2	0	(@ad<"20030626" @rlad<"20030626") and chip adj disable with incompatibl\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/12 14:22
L3	0	(@ad<"20030626" @rlad<"20030626") and chip adj disable with incompatibl\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/12 14:22
L4	30	(@ad<"20030626" @rlad<"20030626") and chip with disable and incompatibl\$5 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/12 14:24
L5	35	(@ad<"20030626" @rlad<"20030626") and chip with disable and incompatibl\$5 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:23
L6	35	(@ad<"20030626" @rlad<"20030626") and chip with disable and incompatibl\$5 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:23
L7	35	(@ad<"20030626" @rlad<"20030626") and chip with disable and incompatibl\$5 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:23
L8	35	(@ad<"20030626" @rlad<"20030626") and chip with disable and incompatibl\$5 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:23
L9	0	8 and (frequency speed) with incompatibility	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:24

EAST Search History

L10	1	8 and (frequency speed) with incompatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:24
L11	18566	(@ad<"20030626" @rlad<"20030626") and (FPGA ASIC microchip chip circuit) with disable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/12 14:25
L12	26054	(@ad<"20030626" @rlad<"20030626") and (FPGA ASIC microchip chip circuit) with disable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:25
L13	45235	(@ad<"20030626" @rlad<"20030626") and (FPGA ASIC microchip chip circuit) with disabl\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:26
L14	5	13 and disabl\$3 same incompatib\$5 same (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/12 14:27